

## Features

- Input Voltage (VCNTL): Supports 3.3V Rail and 5V Rail
- VIN Voltage Range: 1.0V to VCNTL
- VREF Inputs can be given directly or by resistor divider
- Low Output Voltage Offset
- Current Limit Protection
- On-Chip Thermal Protection
- Built-in Soft Start, UVLO
- Stable with Output Ceramic Capacitor
- Low External Components Count
- Supports DDR2, DDR3, and Low-Power DDR3/DDR4 VTT Applications
- PSOP-8 Package
- Green Product (RoHS, Lead-Free, Halogen-Free Compliant)

## Applications

- Mainboard for Chipset
- DDR Memory Systems
- Notebook/Desktop/Server
- Active Termination Buses
- Telecom/Datacom, GSM Base Station, LCD-TV/PDP-TV, Copier/Printer, Set-Top Box

## General Description

The GS7909 is a high performance sink/source Double Data Rate (DDR) termination regulator . It is specifically designed for the quick response, low noise and low external component count systems where space is a key application parameter.

The output termination voltage can be tightly regulated to track the reference input (VREF), and VREF allows for flexible input tracking either directly or through resistor divider.

In addition, the GS7909 can capable of sinking/sourcing up to peak 2A output current. Also, the GS7909 includes current limit and thermal shutdown protections to prevent the regulator from damage. The built-in soft start minimizes stress on the input power source by reduction the capacitive inrush current on start-up.

The GS7909 is available in the thermally efficient PSOP-8 package.